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PATENT NUMBER and  
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U.S. UTILITY Patent Application

APPL NUM 10080396	FILING DATE 02/22/2002	CLASS 365	SUBCLASS 173	GAU 2818	EXAMINER YOH A
**APPLICANTS: Shi Xizeng; Tong Hua-Ching; Menon Aric;					
**CONTINUING DATA VERIFIED: THIS APPLN CLAIMS BENEFIT OF 60/271,207 02/23/2001					
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** FOREIGN APPLICATIONS VERIFIED:					

PG-PUB	DO NOT PUBLISH <input checked="" type="checkbox"/>	RESCIND <input type="checkbox"/>	ATTORNEY DOCKET NO.
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiner's initials <i>Cjoh</i>		<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	RR1724/2344P
TITLE : High capacity MRAM memory array architecture U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)			

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	
ISSUE FEE		Primary Examiner	
Amount Due	Date Paid	PREPARED FOR ISSUE	
TERMINAL		Application Examiner	
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